

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT

Docket: 010251

CLAIMS

1. (Original) A method comprising:
starting a timer defined for use within a first wireless communication system; and
estimating a duration of a transition from the first wireless communication system to a second wireless communication system as a function of the timer.
2. (Original) The method of claim 1, further comprising performing a pre-defined operation associated with the timer.
3. (Original) The method of claim 2, wherein the operation is pre-defined by the first wireless communication system.
4. (Original) The method of claim 1, wherein the timer comprises a supervision timer.
5. (Original) The method of claim 1, wherein the timer is defined by the IS856 wireless communication standard.
6. (Original) The method of claim 1, further comprising:
starting a plurality of timers defined for use within the first wireless communication system; and

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT
Docket: 010251

when returning to the first wireless communication system, estimating the duration of the transition as a function of the plurality of timers.

7. (Original) The method of claim 1, wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system.

8. (Original) The method of claim 7, wherein the supervision timer comprises an IS856 Control Channel Supervision Timer.

9. (Original) The method of claim 8, further comprising:
attempting to receive a synchronous control channel capsule; and
transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

10. (Original) The method of claim 7, wherein the supervision timer comprises a data rate control (DRC) supervision timer, the method further comprising:
starting a combination timer; and
when returning to the IS856 system, estimating the duration of the transition as a function of the DRC supervision timer and the combination timer.

11. (Original) The method of claim 10, further comprising:
restarting a transmitter in response to expiration of the DRC supervision timer; and

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT
Docket: 010251

transitioning to an inactive state in response to expiration of the combination timer.

12. (Original) A processor-readable medium containing processor executable instructions for:

starting a timer defined for use within a first wireless communication system; and

estimating a duration of a transition from the first wireless communication system to a second wireless communication system as a function of the timer.

13. (Original) The processor-readable medium of claim 12, containing further instructions for performing a pre-defined operation associated with the timer.

14. (Original) The processor-readable medium of claim 13, wherein the operation is pre-defined by the first wireless communication system.

15. (Original) The processor-readable medium of claim 12, wherein the timer comprises a supervision timer.

16. (Original) The processor-readable medium of claim 12, wherein the timer is defined by the IS856 wireless communication standard.

17. (Original) The processor-readable medium of claim 12, containing further instructions for:

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT

Docket: 010251

starting a plurality of timers defined for use within the first wireless communication system; and

when returning to the first wireless communication system, estimating the duration of the transition as a function of the plurality of timers.

18. (Original) The processor-readable medium of claim 12, wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system.

19. (Original) The processor-readable medium of claim 18, wherein the supervision timer comprises an IS856 Control Channel Supervision Timer.

20. (Original) The processor-readable medium of claim 19, containing further instructions for:

attempting to receive a synchronous control channel capsule; and

transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

21. (Original) The processor-readable medium of claim 18, wherein the supervision timer comprises a data rate control (DRC) supervision timer, the processor-readable medium containing further instructions for:

starting a combination timer; and

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT
Docket: 010251

when returning to the IS856 system, estimating the duration of the transition as a function of the DRC supervision timer and the combination timer.

22. (Original) The processor-readable medium of claim 21, containing further instructions for:

restarting a transmitter in response to expiration of the DRC supervision timer; and
transitioning to an inactive state in response to expiration of the combination timer.

23. (Original) A wireless communication device comprising:
first wireless communication system hardware for operating in a first wireless communication system;
second wireless communication system hardware for operating in a second wireless communication system;
an interoperation module to configure the wireless communication device in response to a transition between the first and second wireless communication systems, the interoperation module configured to estimate a duration of the transition as a function of a supervision timer.

24. (Original) The wireless communication device of claim 23, wherein the interoperation module is configured to estimate the duration of the transition as a function of a plurality of supervision timers.

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT
Docket: 010251

25. (Original) The wireless communication device of claim 23, wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system.

26. (Original) The wireless communication device of claim 25, wherein the supervision timer is a Control Channel Supervision Timer.

27. (Original) The wireless communication device of claim 26, wherein the interoperation module is configured to:
attempt to receive a synchronous control channel capsule; and
transition to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

28. (Original) The wireless communication device of claim 25, wherein the supervision timer is a data rate control (DRC) supervision timer, and wherein the interoperation module is configured to:
start a combination timer; and
when returning to the IS856 system, estimate the duration of the transition as a function of the DRC supervision timer and the combination timer.

29. (Original) The wireless communication device of claim 28, wherein the interoperation module is configured to:
restart a transmitter in response to expiration of the DRC supervision timer; and

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT

Docket: 010251

transition to an inactive state in response to expiration of the combination timer.

30. (Original) An apparatus comprising:

means for starting a timer defined for use within a first wireless communication system;

and

estimating a duration of a transition from the first wireless communication system to a second wireless communication system as a function of the timer.

31. (Original) The apparatus of claim 30, further comprising means for performing a pre-defined operation associated with the timer.

32. (Original) The apparatus of claim 31, wherein the operation is pre-defined by the first wireless communication system.

33. (Original) The apparatus of claim 30, wherein the timer comprises a supervision timer.

34. (Original) The apparatus of claim 30, wherein the timer is defined by the IS856 wireless communication standard.

35. (Original) The apparatus of claim 34, further comprising:
means for starting a plurality of timers defined for use within the first wireless communication system; and

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT

Docket: 010251

means for estimating the duration of the transition as a function of the plurality of timers when returning to the first wireless communication system.

36. (Original) The apparatus of claim 30, wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system.

37. (Original) The apparatus of claim 36, wherein the supervision timer comprises an IS856 Control Channel Supervision Timer.

38. (Original) The apparatus of claim 37, further comprising:
means for attempting to receive a synchronous control channel capsule; and
means for transitioning to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

39. (Original) The apparatus of claim 36, wherein the supervision timer comprises a data rate control (DRC) supervision timer, the apparatus further comprising:
means for starting a combination timer; and
means for estimating the duration of the transition as a function of the DRC supervision timer and the combination timer when returning to the IS856 system.

40. (Original) The apparatus of claim 39, further comprising:

Appl. No. 10/062,207

Response dated 10/27/04

Reply to Office Action of 09/03/2004

PATENT

Docket: 010251

means for restarting a transmitter in response to expiration of the DRC supervision timer;

and

means for transitioning to an inactive state in response to expiration of the combination timer.

41. (Original) A system comprising:

a memory that stores processor-readable instructions; and

a processor coupled to the memory that executes the instructions to start a timer defined for use within a first wireless communication system and to estimate a duration of a transition from the first wireless communication system to a second wireless communication system as a function of the timer.

42. (Original) The system of claim 41, wherein the processor further executes the instructions to perform a pre-defined operation associated with the timer.

43. (Original) The system of claim 42, wherein the operation is pre-defined by the first wireless communication system.

44. (Original) The system of claim 41, wherein the timer comprises a supervision timer.

45. (Original) The system of claim 41, wherein the timer is defined by the IS856 wireless communication standard.

Appl. No. 10/062,207
Response dated 10/27/04
Reply to Office Action of 09/03/2004

PATENT
Docket: 010251

46. (Original) The system of claim 41, wherein the processor further executes the instructions to:

start a plurality of timers defined for use within the first wireless communication system;

and

when returning to the first wireless communication system, estimate the duration of the transition as a function of the plurality of timers.

47. (Original) The system of claim 41, wherein the first wireless communication system is an IS856 system and the second wireless communication system is an IS2000-1x system.

48. (Original) The system of claim 47, wherein the supervision timer comprises an IS856 Control Channel Supervision Timer.

49. (Original) The system of claim 48, wherein the processor further executes the instructions to:

attempt to receive a synchronous control channel capsule; and

transition to a network acquisition state when the attempt to receive the synchronous control channel capsule is unsuccessful.

Appl. No. 10/062,207
Response dated 10/27/04
Reply to Office Action of 09/03/2004

PATENT
Docket: 010251

50. (Original) The system of claim 47, wherein the supervision timer comprises a data rate control (DRC) supervision timer, and wherein the processor further executes the instructions to:

start a combination timer; and

when returning to the IS856 system, estimate the duration of the transition as a function of the DRC supervision timer and the combination timer.

51. (Original) The system of claim 50, wherein the processor further executes the instructions to:

restart a transmitter in response to expiration of the DRC supervision timer; and

transition to an inactive state in response to expiration of the combination timer.